

FIG.1
(PRIOR ART)

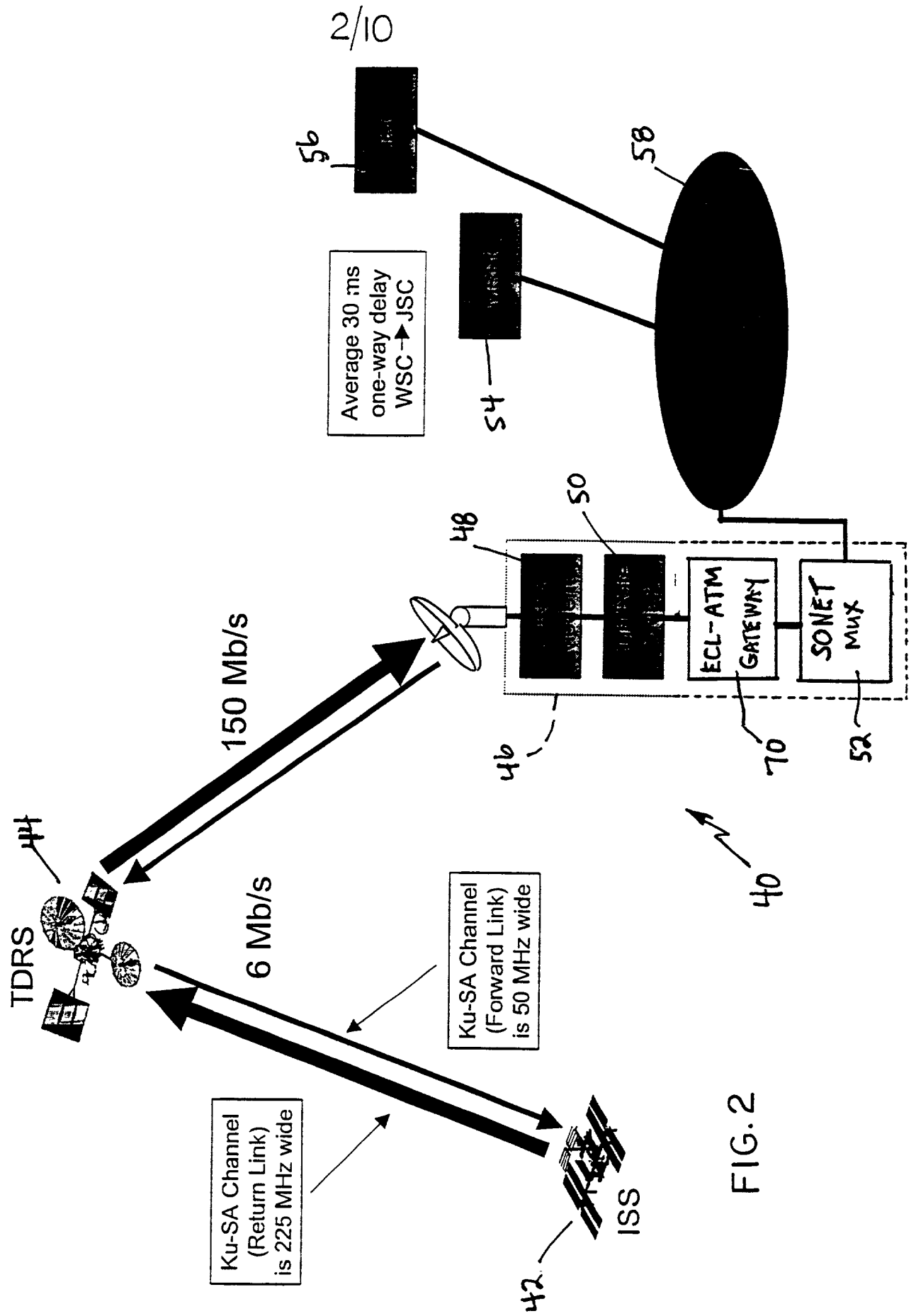


FIG. 2

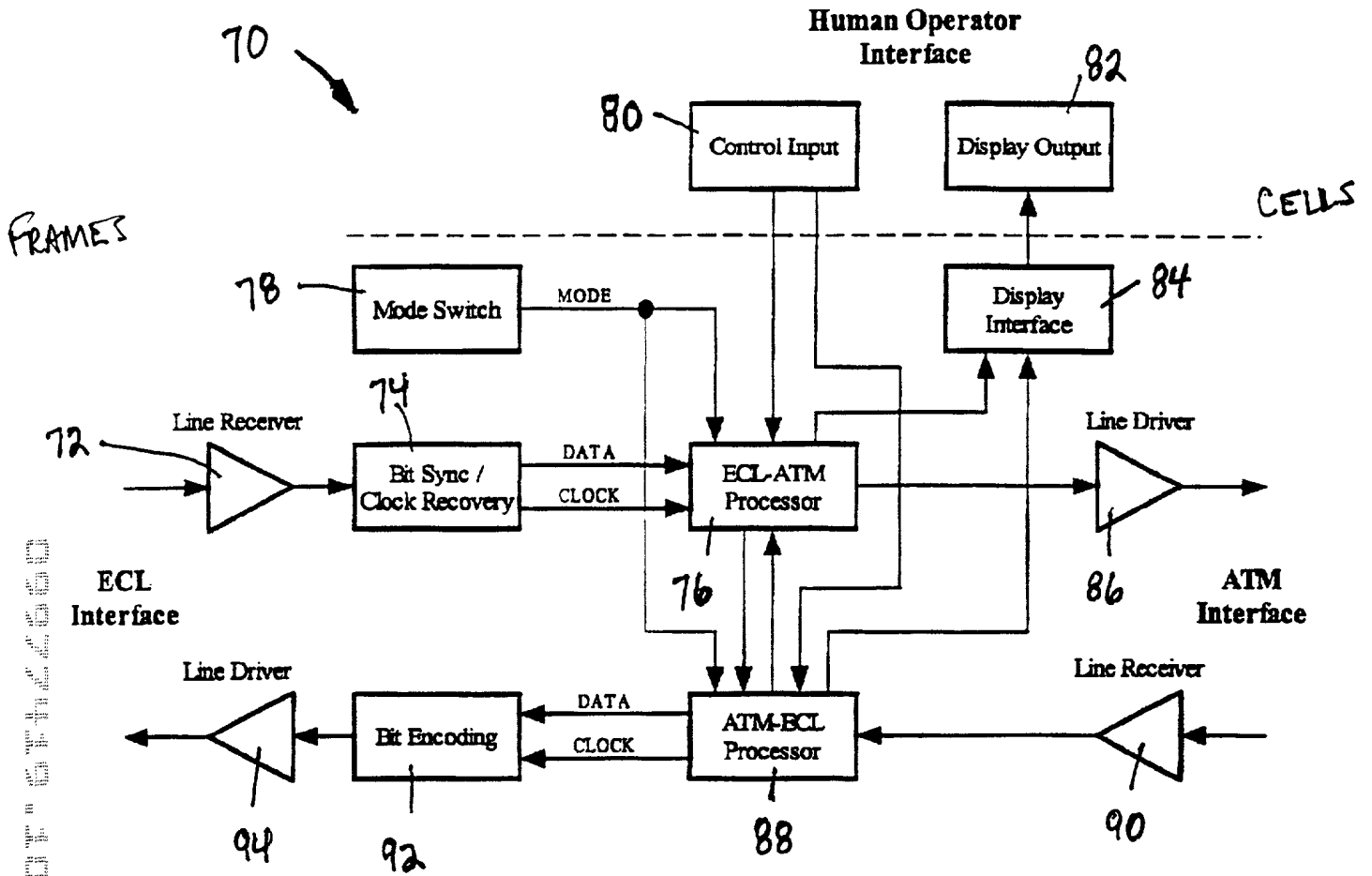


FIG.3

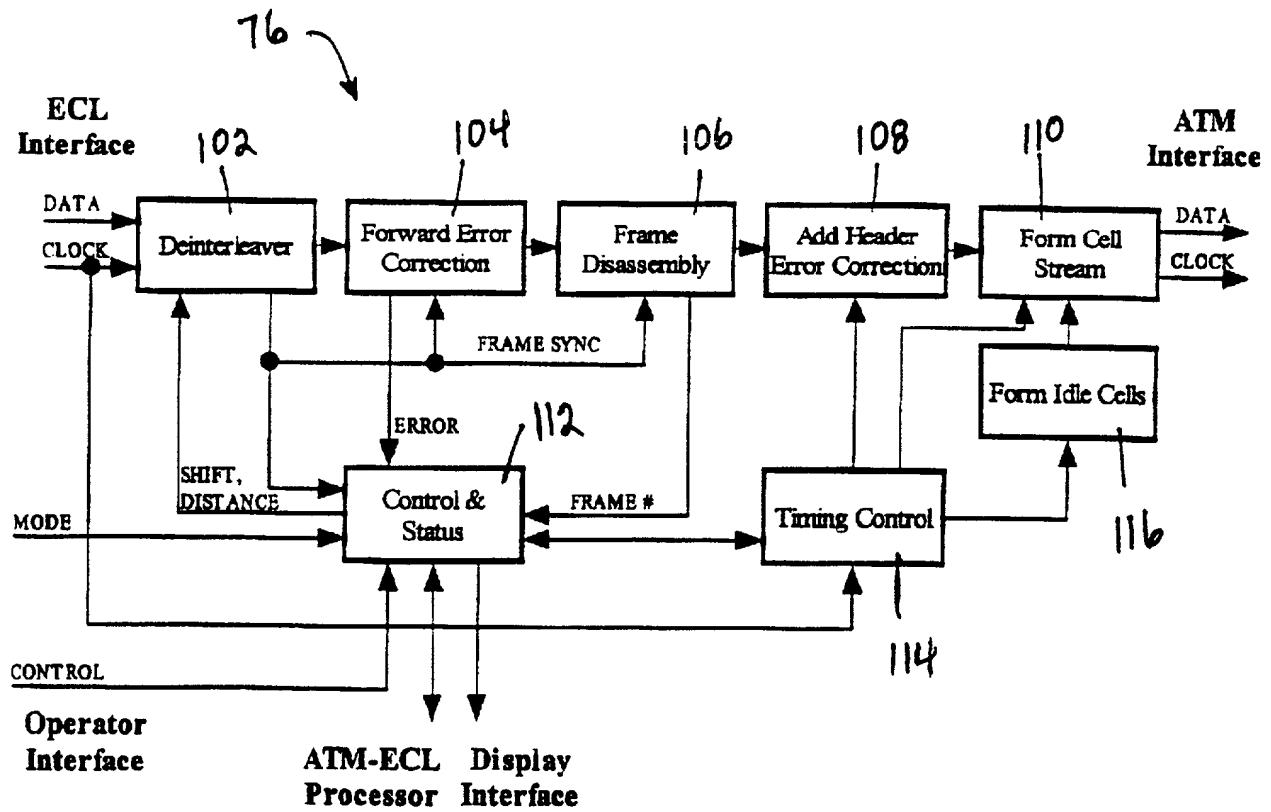


FIG.4

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CELLS

FRAMES

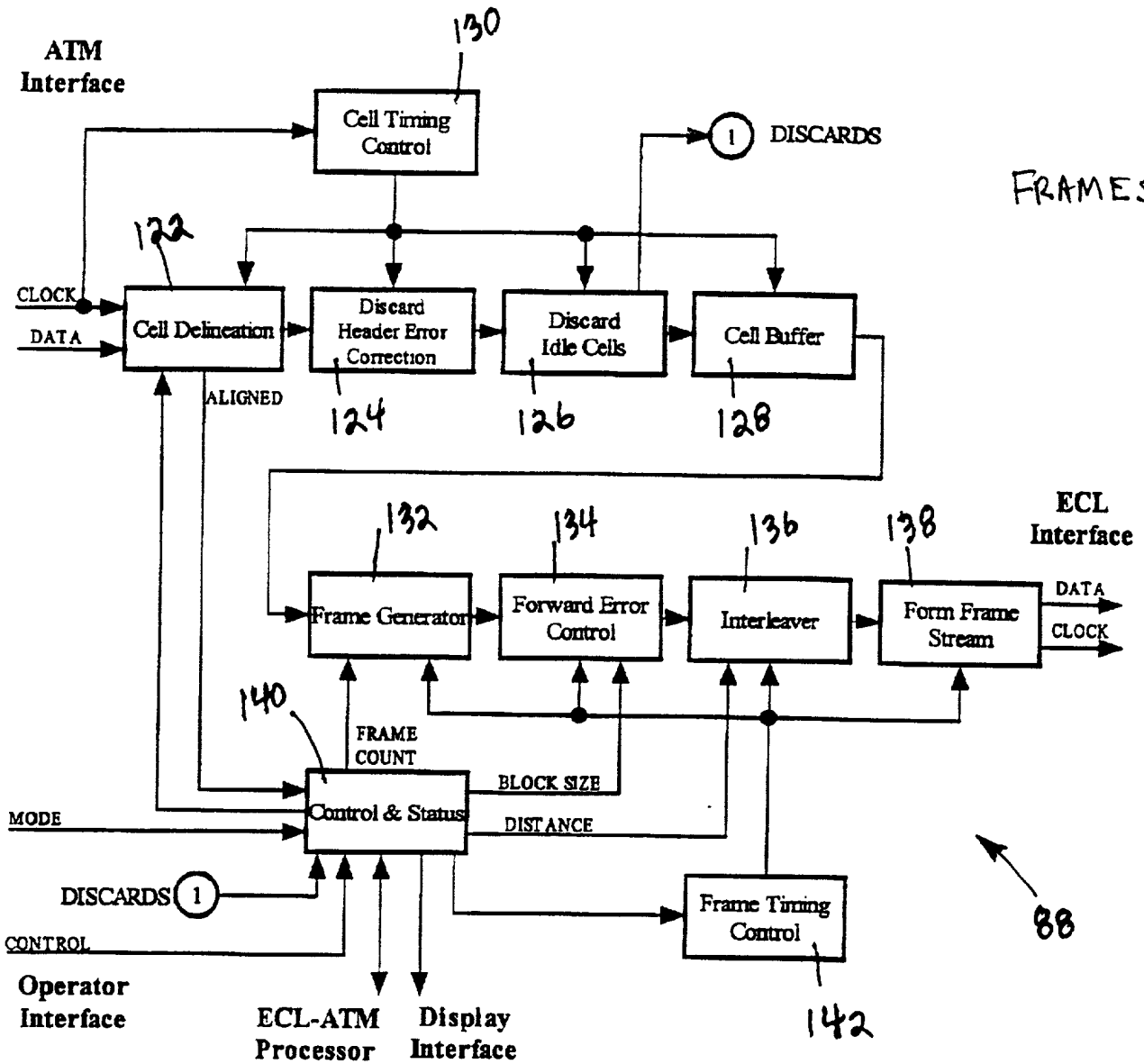


FIG. 5

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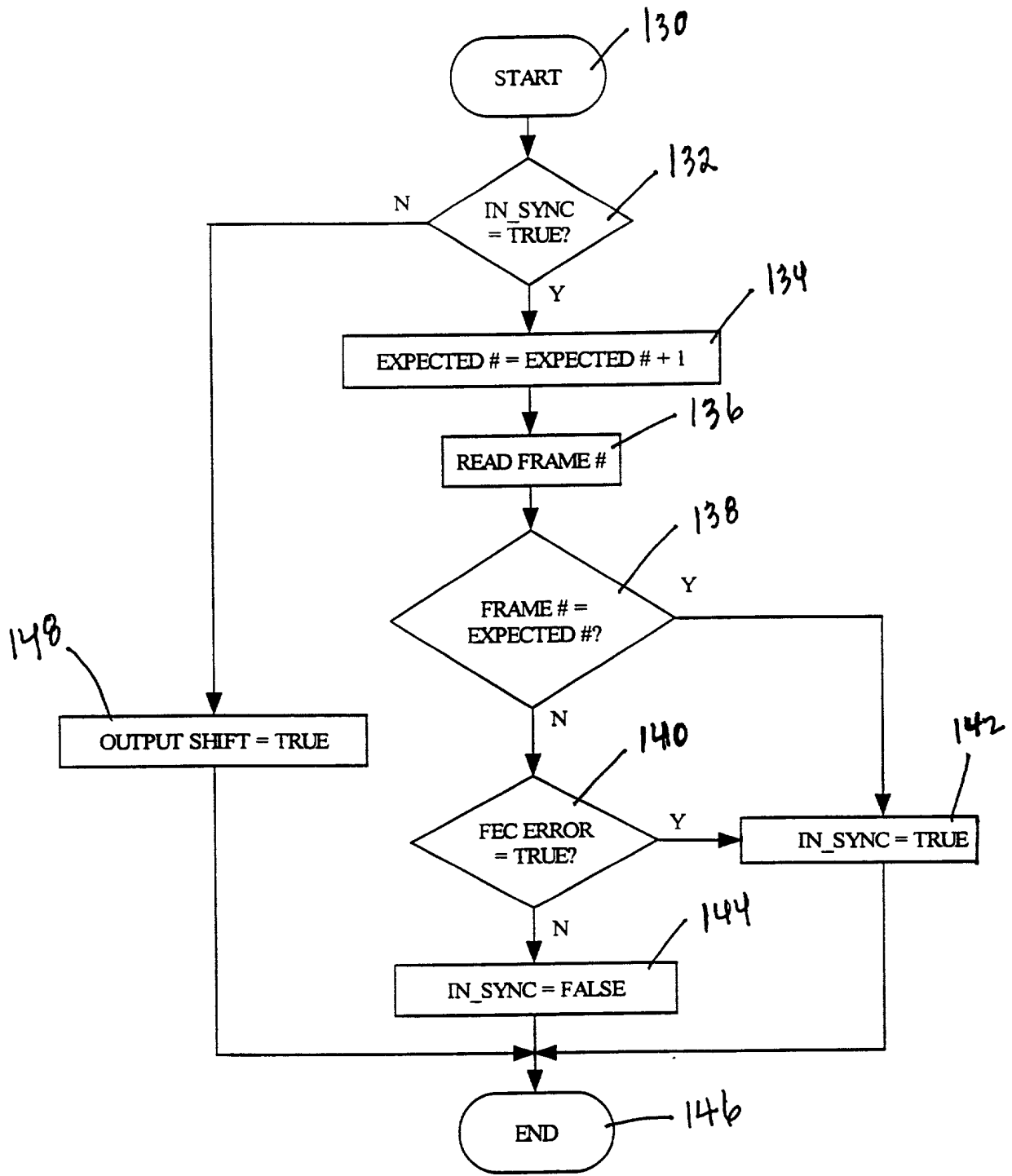


FIG. 6

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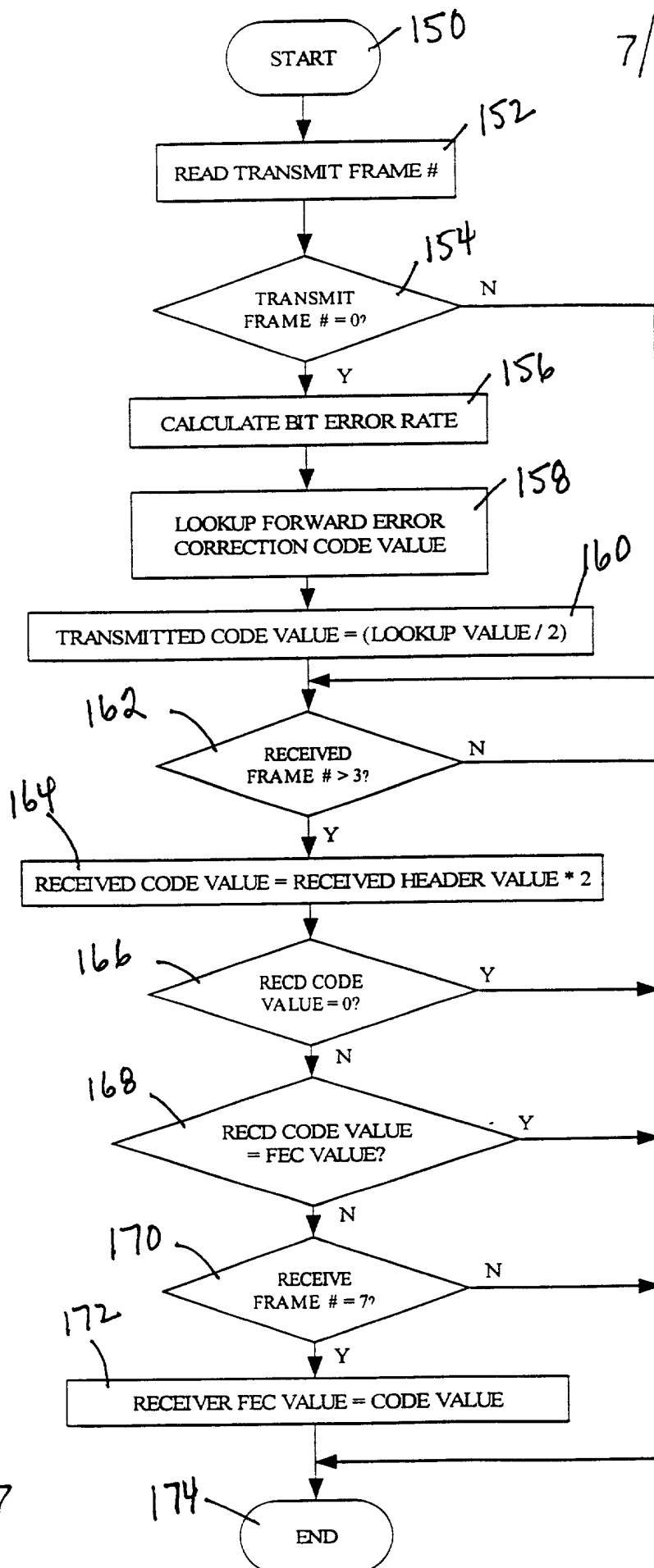


FIG. 7

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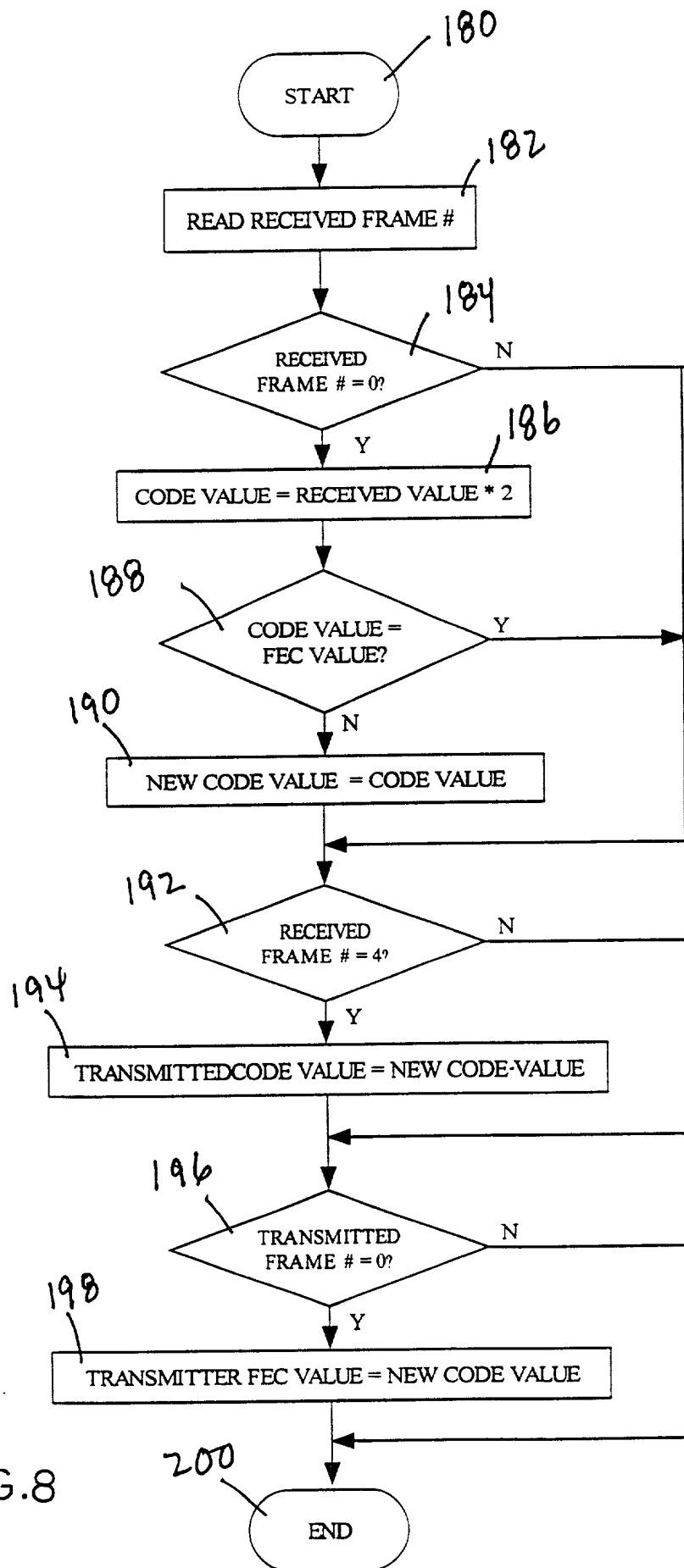


FIG. 8

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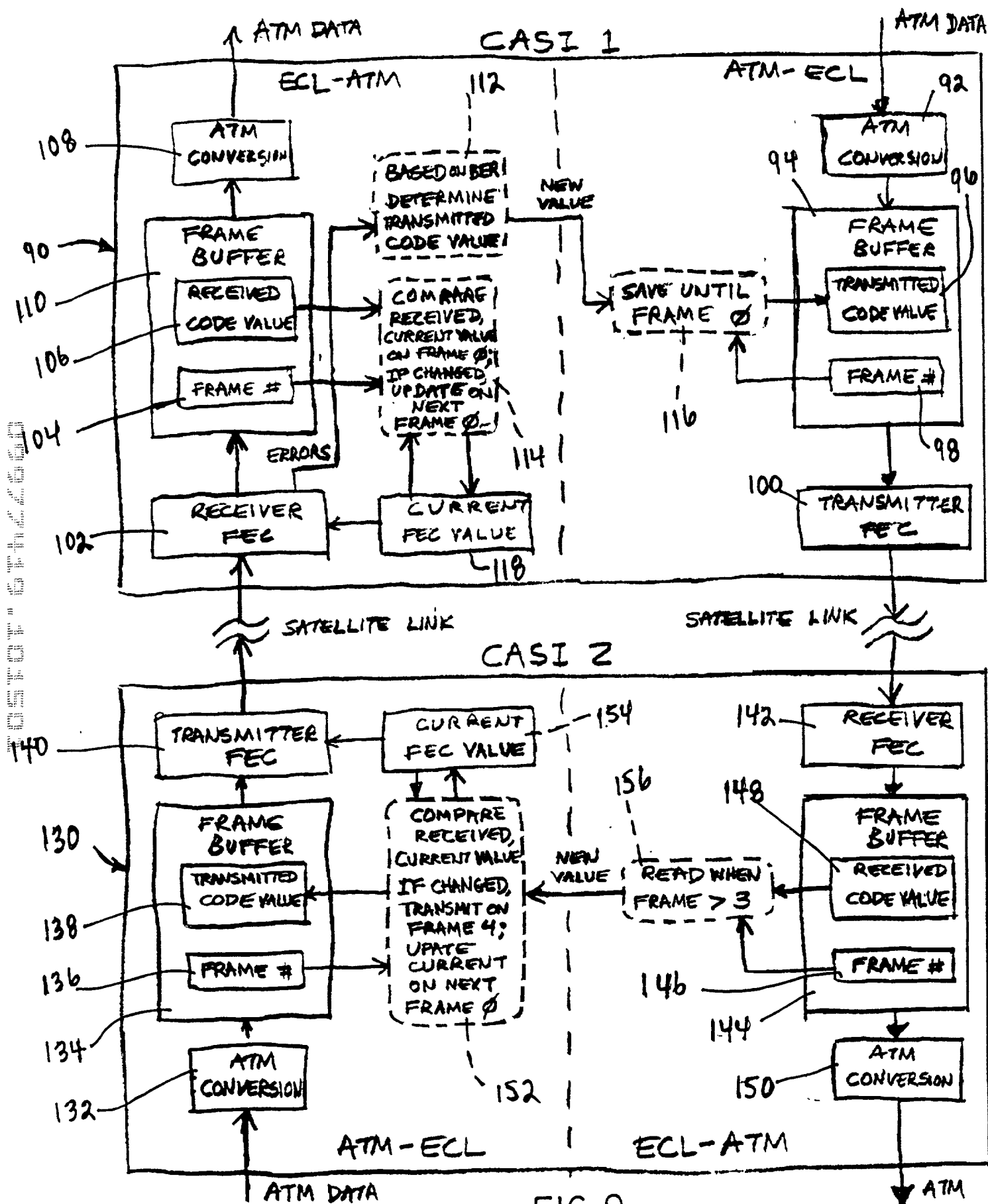
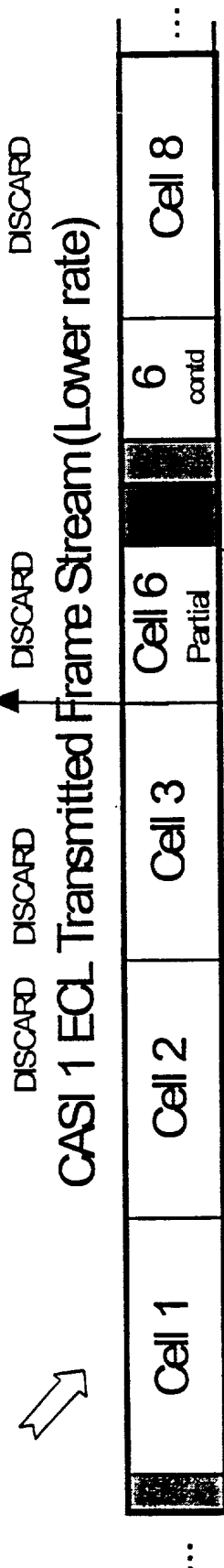
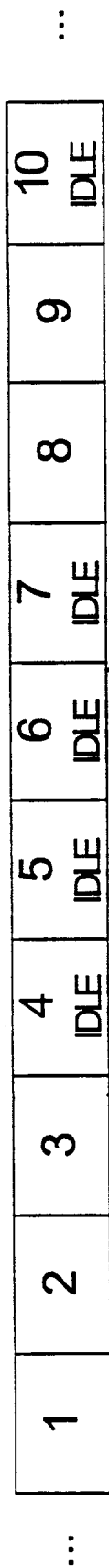


FIG. 9

CASI 1 ATM Input Cell Stream

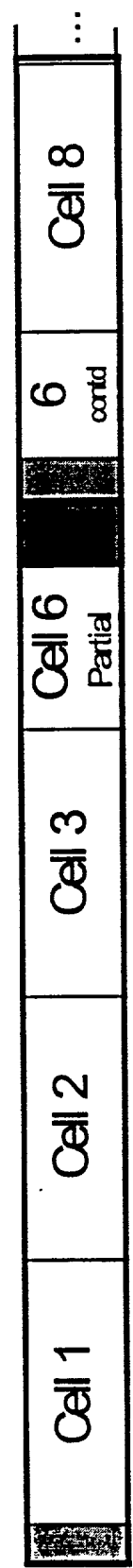


Header Payload FEC

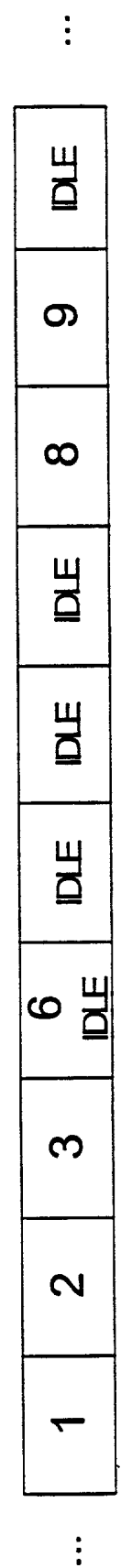


Satellite Link

CASI 2 ECL Received Frame Stream



CASI 2 ATM Output Cell Stream



CASI Data Timing

FIG. 10